

Application Data Sheet
Application Information

Application type::	Regular
Subject matter::	Utility
CD-ROM or CD-R::	None
Sequence submission?::	No
Computer readable form (CRF)?::	No
Title::	An Integrated Scheme for Yield Improvement by Self-Consistent Minimization of IC Design and Process Interactions
Request for early publication?::	No
Request for non-publication?::	No
Suggested drawing figure::	1
Total drawing sheets::	6
Small entity?::	Yes
Petition included?::	No
Secrecy Order in Parent Appl.?::	No

Applicant Information

Applicant authority type::	Inventor
Primary citizenship country::	USA
Status:	Full capacity
Given name::	Valery
Family name::	Axelrad
City of Residence::	Woodside
State or province of residence::	CA
Country of residence::	USA
Street of mailing address::	137 Chapman Rd.
City of mailing address::	Woodside

State or province of mailing address::	CA
Country of mailing address::	USA
Postal or zip code of mailing address::	94062
Applicant authority type::	Inventor
Primary citizenship country::	USA
Status:	Full capacity
Given name::	Victor
Family name::	Boksha
City of Residence::	Los Altos
State or province of residence::	CA
Country of residence::	USA
Street of mailing address::	932 Lundy Lane
City of mailing address::	Los Altos
State or province of mailing address::	CA
Country of mailing address::	USA
Postal or zip code of mailing address::	94024
Applicant authority type::	Inventor
Primary citizenship country::	Russia
Status:	Full capacity
Given name::	Andrei
Family name::	Shibkov
City of Residence::	San Jose
State or province of residence::	CA
Country of residence::	USA
Street of mailing address::	1336 Fruitdale Av. A3
City of mailing address::	San Jose

State or province of mailing address:: CA
Country of mailing address:: USA
Postal or zip code of mailing address:: 95126

Correspondence Information

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Domestic Priority Information			
Application::	Continuity Type::	Parent Application::	Parent Filing Date::
This application	An application claiming benefit under 35 USC 119(e)	60/451,428	03/03/03